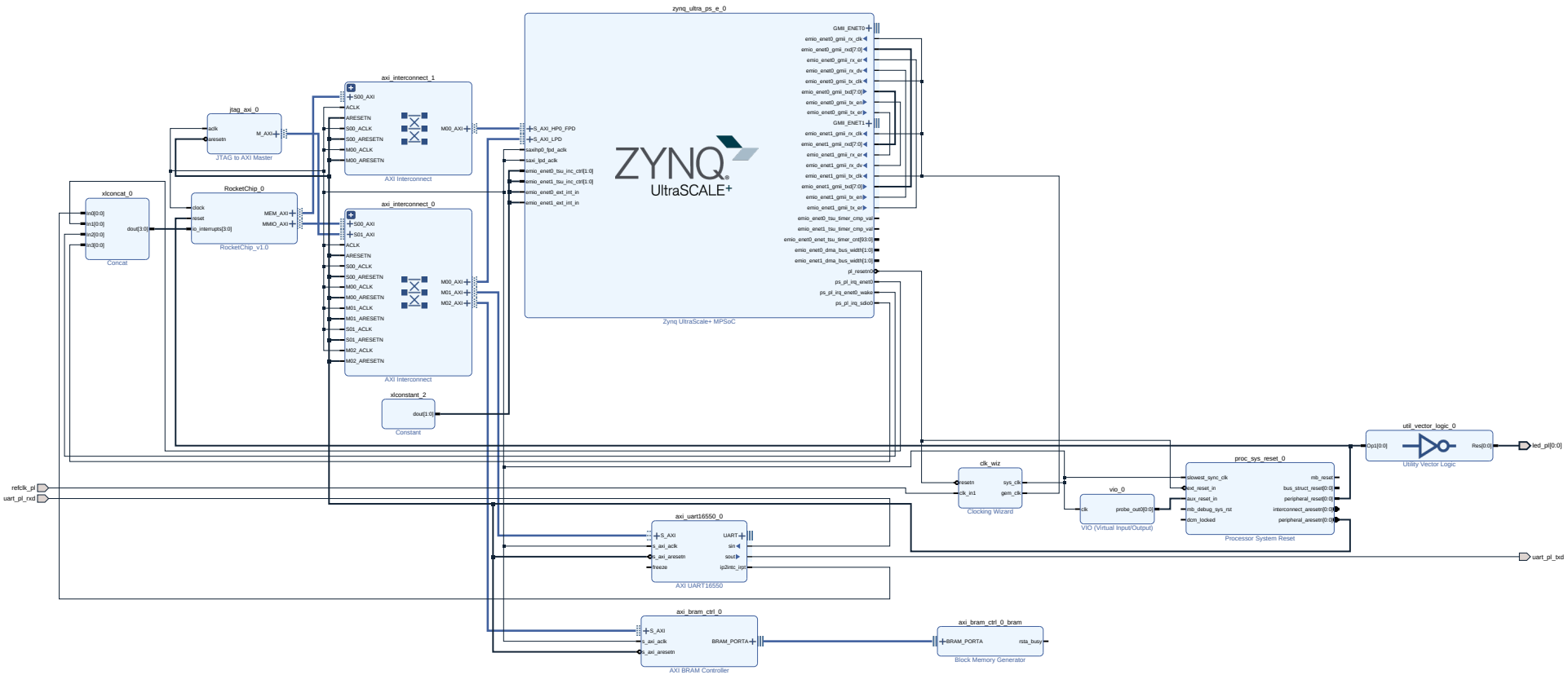
zynq_ultra_ps_e_0
ZYNQ
UltraSCALE+
Zynq UltraScale+ MPSoC
axi_interconnect_1
AXI Interconnect
axi_interconnect_0
AXI Interconnect
jtag_axi_0
JTAG to AXI Master
RocketChip_0
RocketChip_v1.0
xlconcat_0
Concat
xlconstant_2
Constant
util_vector_logic_0
Utility Vector Logic
led_pl[0:0]
clk_wiz
Clocking Wizard
vio_0
VIO (Virtual Input/Output)
proc_sys_reset_0
Processor System Reset
axi_uart16550_0
AXI UART16550
uart_pl_txd
axi_bram_ctrl_0
AXI BRAM Controller
axi_bram_ctrl_0_bram
Block Memory Generator
refclk_pl
uart_pl_rxd